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Terms	Documents
overflow same data same bus same (alter\$3 or change\$3) same mode	63

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Search Results -

Terms	Documents
overflow same data same bus same (alter\$3 or change\$3) same mode	11

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L2   overflow same data same bus same (alter\$3 or change\$3) same mode

11   L2

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1   overflow same data same bus same (alter\$3 or change\$3) same mode

63   L1

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Search Results -

Terms	Documents
L1 same (control\$4 near10 flow)	6

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L4	11 same (control\$4 near10 flow)	6	L4
L3	11 same (control\$4 near5 flow)	6	L3
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L2	overflow same data same bus same (alter\$3 or change\$3) same mode	11	L2
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L1	overflow same data same bus same (alter\$3 or change\$3) same mode	63	L1

END OF SEARCH HISTORY







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```
11 same {control$4 near10 flow}
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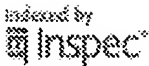
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IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
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- ☐
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[Data Compression Conference, 1995. DCC '95. Proceedings](#)  
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Wireless video coding system demonstration

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This paper appears in: Data Compression Conference, 1995. DCC '95. Proceedings

Publication Date: 28-30 March 1995

On page(s): 448

Meeting Date: 03/28/1995 - 03/30/1995

Location: Snowbird, UT

INSPEC Accession Number: 5086214

Digital Object Identifier: 10.1109/DCC.1995.515558

Posted online: 2002-08-06 20:02:26.0

Abstract

Summary form only given. We have developed and present here a prototype point-to-point wireless video system that has been implemented using a combination of commercial components and custom hardware. The coding algorithm being used consists of subband decomposition using low-complexity, integer-coefficient filters, scalar quantization, and run-length and entropy coding. The prototype system consists of the following major components: spread spectrum radio with interface card and driver, compression board, and an NEC laptop and docking station which provide the PC bus slots and control. The compression algorithms are implemented on a board with a single 10000-gate FPGA. Prior to implementing the algorithms in hardware, a study was performed to resolve issues of word length and scaling, and to select quantization and run length parameters. It was determined that 16-bit precision in the wavelet transform stage is sufficient to prevent under-low and overflow provided that rescaling of data is correctly performed. After processing by the FPGA, the compressed video is transferred to the PC for transmission over the radio. A commercial serial card (PI Card) provides a synchronous serial interface to the radio. The serial controller chip used by this card supports several serial protocols and thus the effect of the these protocols on the data in a wireless environment can be tested

Index Terms  
Inspec

Controlled Indexing

digital filters entropy codes laptop computers microcomputer applications quantisation (signal) runlength codes spread spectrum communication telecommunication computing telecommunication control video coding

Non-controlled Indexing

16 bit FPGA NEC laptop PC bus control PC bus slots coding algorithm compression board docking station driver entropy coding integer-coefficient filters interface card point-to-point wireless video system run-length coding scalar quantization scaling serial controller chip spread spectrum radio subband decomposition synchronous serial interface system demonstration wireless video coding word length

Author Keywords

Not Available

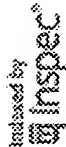
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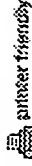
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## Wireless video coding system demonstration

Villaseñor, J., Jain, R., Belzer, B., Boring, W., Chien, C., Jones, C., Liao, J., Molloy, S., Nazareth, S., Schoner, B., Stod, J., Dept. of Electr. Eng., California Univ., Los Angeles, CA, USA;

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Index Terms

Inspec

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### Non-controlled Indexing

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